## Slater & Matsil, L.L.P.

**Suite 1000** 17950 Preston Road Dallas, Texas 75252-5793

CENTRAL FAX CENTER AUG 0 9 2004

Phone: 972-732-1001 Facsimile: 972-732-9218

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Kowalski, et al.

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Art Unit:

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TITLE:

Device Architecture and Process for Improved Vertical Memory Arrays

## CERTIFICATION OF FACSIMILE TRANSMISSION

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Respectfully submitted.

latabi Swider

Natalie Swider Legal Assistant

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